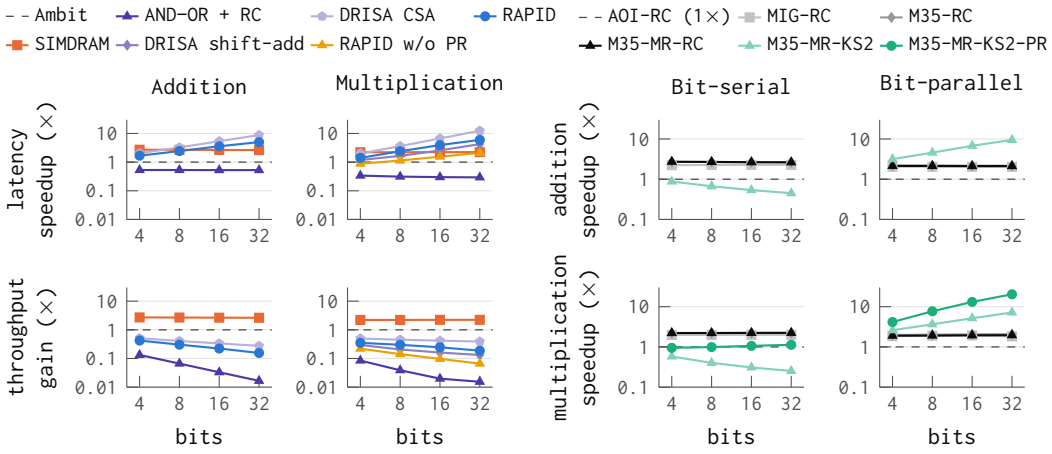

Ambit
AND-OR + RC
DRISA CSA
RAPID
SIMDRAM
DRISA shift-add
RAPID w/o PR
Addition
Multiplication
latency speedup (×)
throughput gain (×)
10
1
0.1
0.01
4
8
16
32
bits
(a)
AOI-RC (1×)
MIG-RC
M35-RC
M35-MR-RC
M35-MR-KS2
M35-MR-KS2-PR
Bit-serial
Bit-parallel
addition speedup (×)
multiplication speedup (×)
(b)